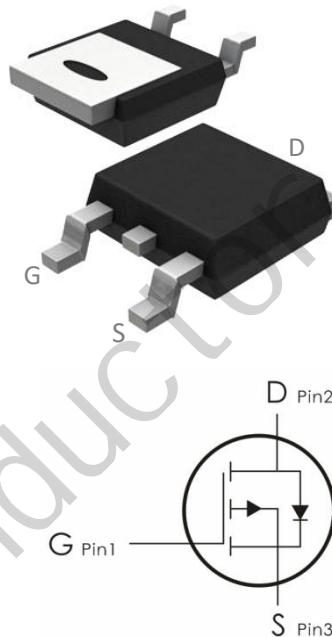


Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=-30V, I_D=-100A, R_{DS(ON)}<5.3m\Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	-100	A
	Continuous Drain Current- $T_C=100^\circ C$	-65	A
I_{DM}	Pulsed Drain Current ^{note1}	-400	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	225	mJ
P_D	Total Power Dissipation	103	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case	1.46	$^\circ C/W$

Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	-30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-30\text{V}, T_J=25^\circ\text{C}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	-1.0	-1.5	-2.5	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ^{note3}	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-30\text{A}$	---	4	5.3	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-20\text{A}$	---	5.8	8.2	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	9200	---	pF
C_{oss}	Output Capacitance		---	980	---	
C_{rss}	Reverse Transfer Capacitance		---	750	---	
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-30\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{G}}=2.5\Omega$	---	13	---	ns
t_r	Rise Time ^{2,3}		---	15	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	65	---	ns
t_f	Fall Time ^{2,3}		---	25	---	ns
Q_g	Total Gate Charge ^{2,3}	$V_{\text{GS}}=-15\text{V}, V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-30\text{A}$	---	41	---	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	8.2	---	nC
Q_{gd}	Gate-Drain "Miller" Charge ^{2,3}		---	11	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Drain Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-30\text{A}, T_J=25^\circ\text{C}$	---	-0.8	-1.2	V
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}, \text{Force Current}$	---	---	-100	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	-400	A

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}= -15\text{V}$, $V_G= -10\text{V}$, $R_{\text{G}}=25\Omega$, $L=0.5\text{mH}$, $I_{\text{AS}}= -30\text{A}$

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

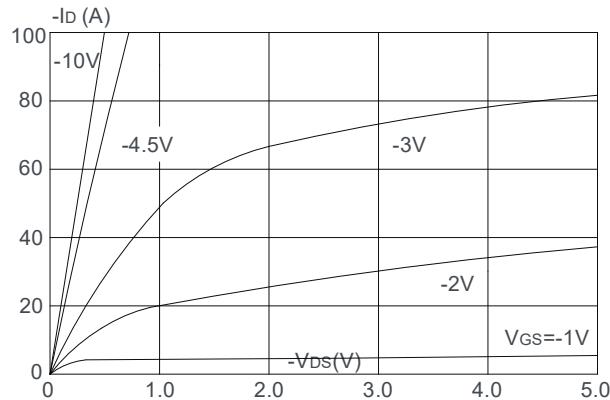


Figure 1: Output Characteristics

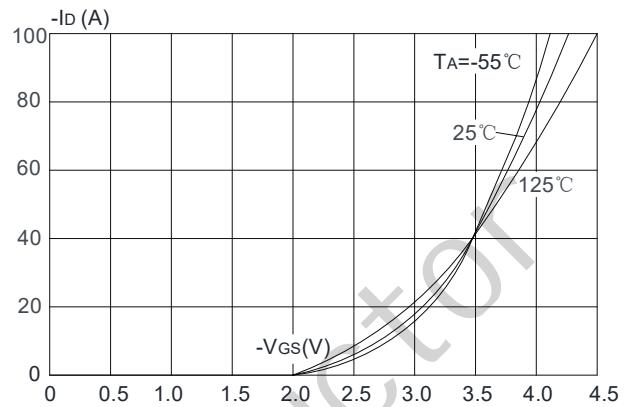


Figure 2: Typical Transfer Characteristics

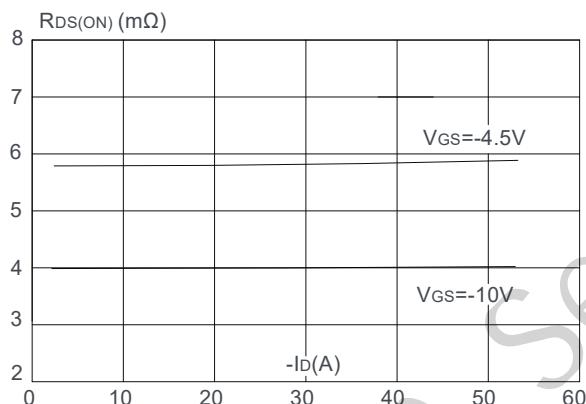


Figure 3: On-resistance vs. Drain Current

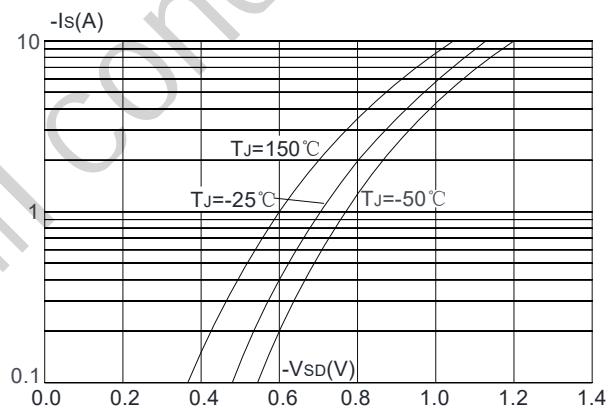


Figure 4: Body Diode Characteristics

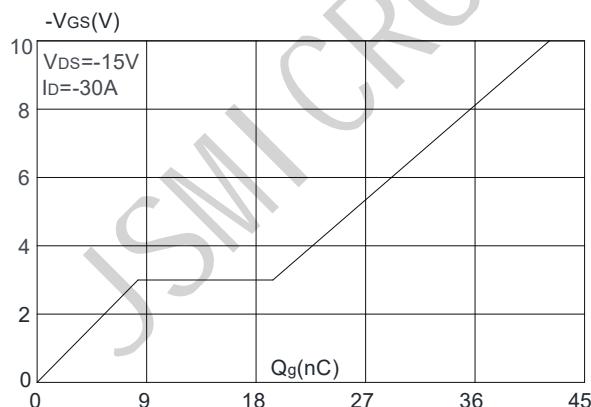


Figure 5: Gate Charge Characteristics

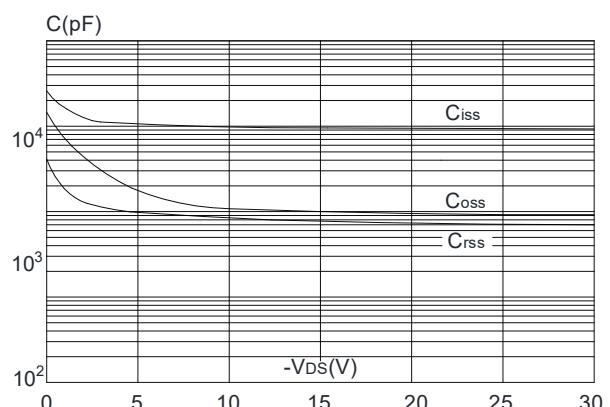


Figure 6: Capacitance Characteristics

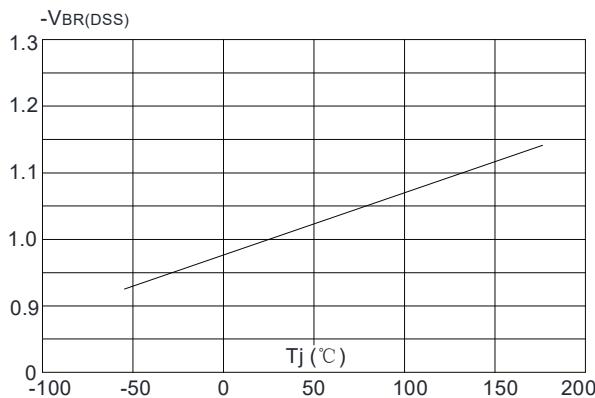


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

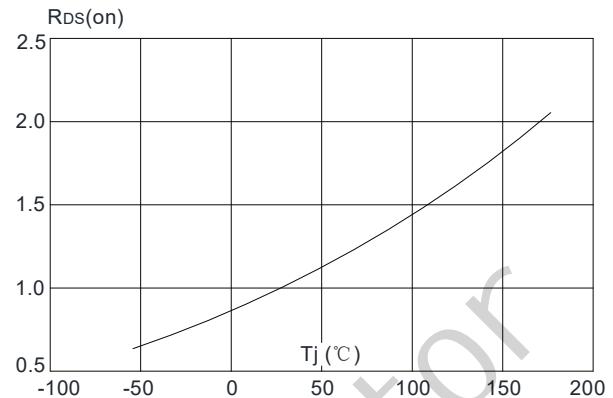


Figure 8: Normalized on Resistance vs. Junction Temperature

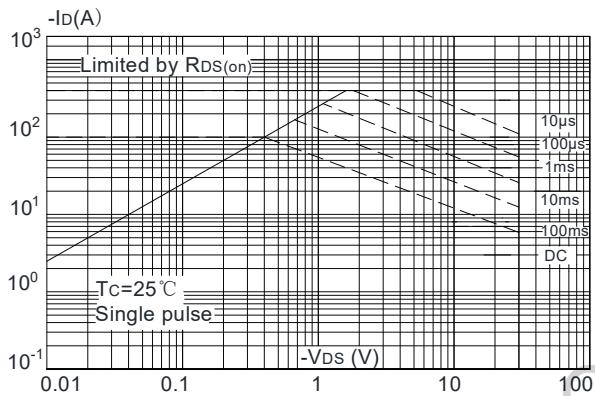


Figure 9: Maximum Safe Operating Area

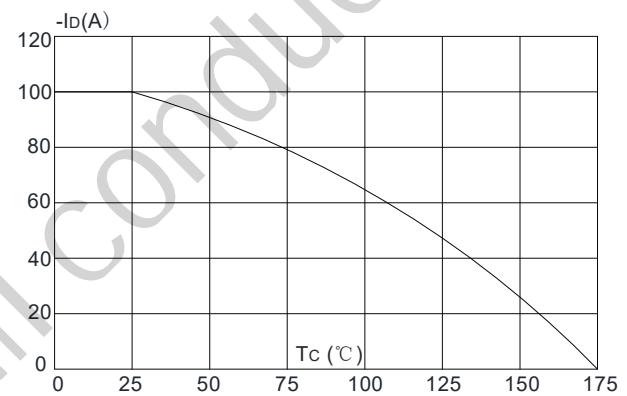


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

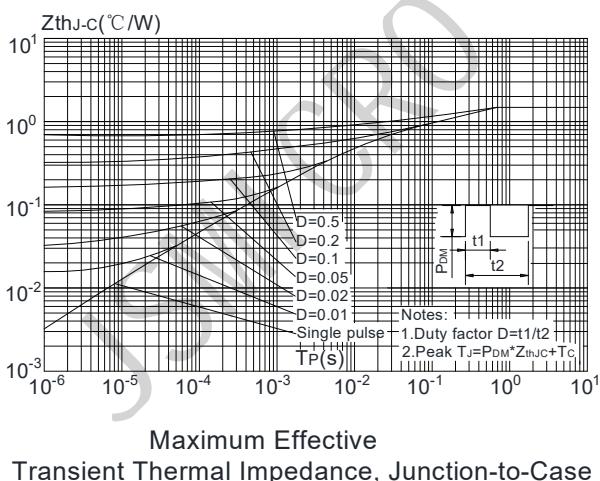


Figure 11:

外形尺寸图 / Package Dimensions

